

Form PTO-1449 (modified)



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| Atty. Docket No. 2000.092400/TT5002 | Serial No. 10/084,987 |
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List of Patents and Publications for Applicant

SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant

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|-----------------------------------|----------------|
| Filing Date: February 28, 2002 | Group: 2877 |
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U.S. Patent Documents

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Foreign Patent Documents

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Other Art

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U.S. Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Filing Date of App. |
|-------------|-----------|-----------------|------|------|-------|-----------|---------------------|
| | A1 | | | | | | |
| | A2 | | | | | | |
| | A3 | | | | | | |
| | A4 | | | | | | |
| | A5 | | | | | | |

Foreign Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Country | Class | Sub Class | Translation Yes/No |
|-------------|-----------|-----------------|---------|---------|-------|-----------|--------------------|
| QW | B1 | EP 0 727 715 A1 | 8/21/96 | Europe | G03F | 7/20 | Yes |
| QM | B2 | WO 01/23871 A1 | 4/05/01 | PCT | G01N | 21/95 | Yes |
| QW | B3 | WO 99/45340 | 9/10/99 | PCT | G01B | 11/02 | Yes |

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

| Exam. Init. | Ref. Des. | Citation |
|-------------|-----------|---|
| QW | C1 | Pfitzner et al., "Cost reduction strategies for wafer expenditure," <i>Microelectronic Engineering</i> , 56:61-71, 2001 |
| | C2 | |
| | C3 | |

EXAMINER:

DATE CONSIDERED:

14 April 2004

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